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Bib Data Sheet

CONFIRMATION NO. 7187

SERIAL NUMBER 09/678,609	FILING OR 371(c) DATE 10/04/2000 RULE	CLASS 257	GROUP ART UNIT 2822	ATTORNEY DOCKET NO. PF-2683/NEC/US/mh
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APPLICANTS

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** CONTINUING DATA *****

MUR

** FOREIGN APPLICATIONS *****

JAPAN 11-284566 10/05/1999

JAPAN 2000-057767 03/02/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

11/29/2000

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 158	TOTAL CLAIMS 79	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature 				

ADDRESS

466

TITLE

MULTILAYER INTERCONNECTION BOARD, SEMICONDUCTOR DEVICE HAVING THE SAME, AND METHOD OF FORMING THE SAME AS WELL AS METHOD OF MOUNTING THE SEMICONDUCTOR CHIP ON THE INTERCONNECTION BOARD

FILING FEE RECEIVED 2016	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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